

LS730 WHL Schematics

2017-11-27

REV : 17934-1

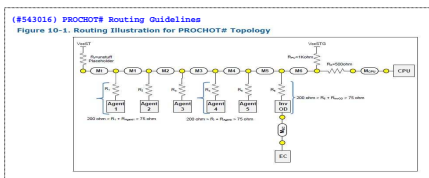
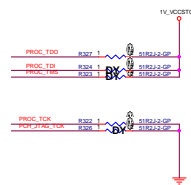
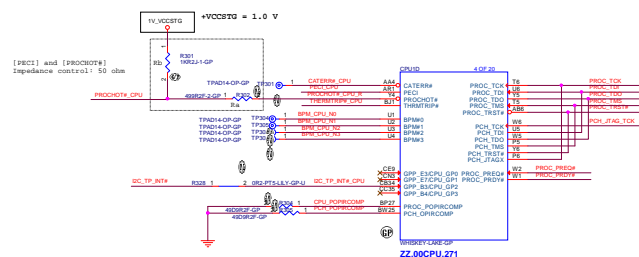
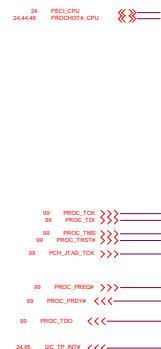
<Variant Name>

緯創資通		Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.
Title COVER PAGE		
Size A4	Document Number S730	Rev -1
Date: Wednesday, July 25, 2018		Sheet 1 of 106

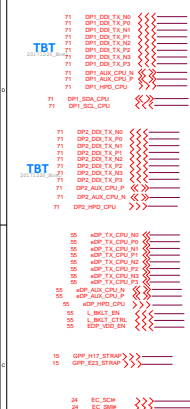
Revision: -1



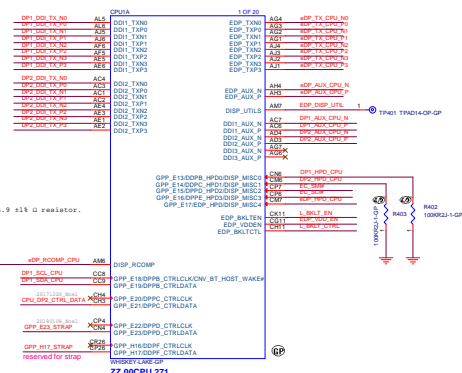
24 PECL_CPU
24,44,46 PROCHOT#_CPU



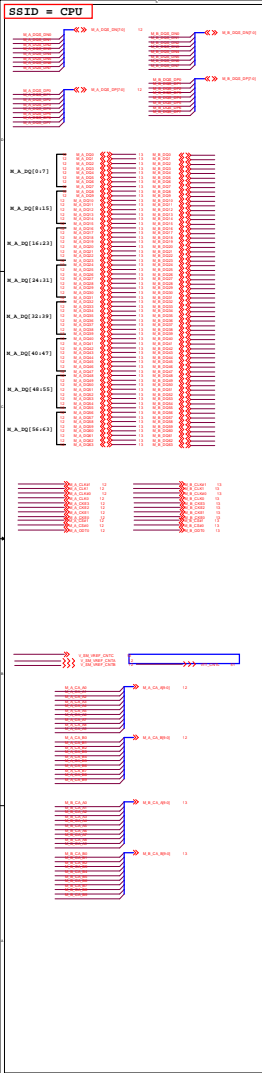
M1,2,3,4,5: <3 inches
M6: 1-11 inches
MCPU: 0.3-1.5 inches
Mt <0.3 mils
Main route(M1+M2+M3+M4+M5+M6+MCPU): 1-12 inches



Design Guideline:
Skylake processor signal eDP_RCOMP should be connected to the VCCIO rail via a single 24.9 $\pm 1\%$ Ω resistor.



Port	Strap	Enable Port	Disable Port
Port 1	DDPS_CTLDATA	PU to 3.3 V with 2.2-k 15 Ω resistor	MC
Port 2	DDPC_CTLDATA	PU to 3.3 V with 2.2-k 15 Ω resistor	MC



Vinafix.com

LPDDR3 ball type: Non-Interleaved Type

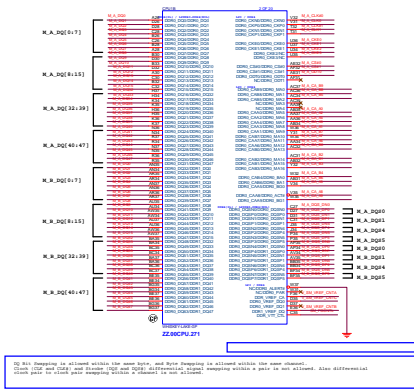
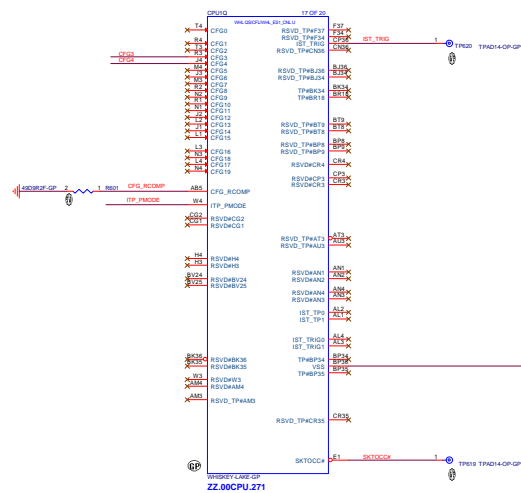


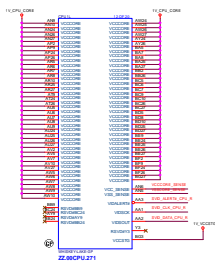
Table 5-68. QDT Signal Connectivity Table (Sheet 2 of 3)

Process	Pin	Mode	Signal	Mode	Pin
CPU	12	Power	SSID	Power	12
	13	Power	SSID	Power	13
CPU	14	Power	SSID	Power	14
	15	Power	SSID	Power	15
CPU	16	Power	SSID	Power	16
	17	Power	SSID	Power	17
CPU	18	Power	SSID	Power	18
	19	Power	SSID	Power	19
CPU	20	Power	SSID	Power	20
	21	Power	SSID	Power	21
CPU	22	Power	SSID	Power	22
	23	Power	SSID	Power	23
CPU	24	Power	SSID	Power	24
	25	Power	SSID	Power	25
CPU	26	Power	SSID	Power	26
	27	Power	SSID	Power	27
CPU	28	Power	SSID	Power	28
	29	Power	SSID	Power	29
CPU	30	Power	SSID	Power	30
	31	Power	SSID	Power	31
CPU	32	Power	SSID	Power	32
	33	Power	SSID	Power	33
CPU	34	Power	SSID	Power	34
	35	Power	SSID	Power	35
CPU	36	Power	SSID	Power	36
	37	Power	SSID	Power	37
CPU	38	Power	SSID	Power	38
	39	Power	SSID	Power	39
CPU	40	Power	SSID	Power	40
	41	Power	SSID	Power	41
CPU	42	Power	SSID	Power	42
	43	Power	SSID	Power	43
CPU	44	Power	SSID	Power	44
	45	Power	SSID	Power	45
CPU	46	Power	SSID	Power	46
	47	Power	SSID	Power	47
CPU	48	Power	SSID	Power	48
	49	Power	SSID	Power	49
CPU	50	Power	SSID	Power	50
	51	Power	SSID	Power	51
CPU	52	Power	SSID	Power	52
	53	Power	SSID	Power	53
CPU	54	Power	SSID	Power	54
	55	Power	SSID	Power	55
CPU	56	Power	SSID	Power	56
	57	Power	SSID	Power	57
CPU	58	Power	SSID	Power	58
	59	Power	SSID	Power	59
CPU	60	Power	SSID	Power	60
	61	Power	SSID	Power	61
CPU	62	Power	SSID	Power	62
	63	Power	SSID	Power	63
CPU	64	Power	SSID	Power	64
	65	Power	SSID	Power	65
CPU	66	Power	SSID	Power	66
	67	Power	SSID	Power	67
CPU	68	Power	SSID	Power	68
	69	Power	SSID	Power	69
CPU	70	Power	SSID	Power	70
	71	Power	SSID	Power	71
CPU	72	Power	SSID	Power	72
	73	Power	SSID	Power	73
CPU	74	Power	SSID	Power	74
	75	Power	SSID	Power	75
CPU	76	Power	SSID	Power	76
	77	Power	SSID	Power	77
CPU	78	Power	SSID	Power	78
	79	Power	SSID	Power	79
CPU	80	Power	SSID	Power	80
	81	Power	SSID	Power	81
CPU	82	Power	SSID	Power	82
	83	Power	SSID	Power	83
CPU	84	Power	SSID	Power	84
	85	Power	SSID	Power	85
CPU	86	Power	SSID	Power	86
	87	Power	SSID	Power	87
CPU	88	Power	SSID	Power	88
	89	Power	SSID	Power	89
CPU	90	Power	SSID	Power	90
	91	Power	SSID	Power	91
CPU	92	Power	SSID	Power	92
	93	Power	SSID	Power	93
CPU	94	Power	SSID	Power	94
	95	Power	SSID	Power	95
CPU	96	Power	SSID	Power	96
	97	Power	SSID	Power	97
CPU	98	Power	SSID	Power	98
	99	Power	SSID	Power	99
CPU	100	Power	SSID	Power	100
	101	Power	SSID	Power	101
CPU	102	Power	SSID	Power	102
	103	Power	SSID	Power	103
CPU	104	Power	SSID	Power	104
	105	Power	SSID	Power	105
CPU	106	Power	SSID	Power	106
	107	Power	SSID	Power	107
CPU	108	Power	SSID	Power	108
	109	Power	SSID	Power	109
CPU	110	Power	SSID	Power	110
	111	Power	SSID	Power	111
CPU	112	Power	SSID	Power	112
	113	Power	SSID	Power	113
CPU	114	Power	SSID	Power	114
	115	Power	SSID	Power	115
CPU	116	Power	SSID	Power	116
	117	Power	SSID	Power	117
CPU	118	Power	SSID	Power	118
	119	Power	SSID	Power	119
CPU	120	Power	SSID	Power	120
	121	Power	SSID	Power	121
CPU	122	Power	SSID	Power	122
	123	Power	SSID	Power	123
CPU	124	Power	SSID	Power	124
	125	Power	SSID	Power	125
CPU	126	Power	SSID	Power	126
	127	Power	SSID	Power	127
CPU	128	Power	SSID	Power	128
	129	Power	SSID	Power	129
CPU	130	Power	SSID	Power	130
	131	Power	SSID	Power	131
CPU	132	Power	SSID	Power	132
	133	Power	SSID	Power	133
CPU	134	Power	SSID	Power	134
	135	Power	SSID	Power	135
CPU	136	Power	SSID	Power	136
	137	Power	SSID	Power	137
CPU	138	Power	SSID	Power	138
	139	Power	SSID	Power	139
CPU	140	Power	SSID	Power	140
	141	Power	SSID	Power	141
CPU	142	Power	SSID	Power	142
	143	Power	SSID	Power	143
CPU	144	Power	SSID	Power	144
	145	Power	SSID	Power	145
CPU	146	Power	SSID	Power	146
	147	Power	SSID	Power	147
CPU	148	Power	SSID	Power	148
	149	Power	SSID	Power	149
CPU	150	Power	SSID	Power	150
	151	Power	SSID	Power	151
CPU	152	Power	SSID	Power	152
	153	Power	SSID	Power	153
CPU	154	Power	SSID	Power	154
	155	Power	SSID	Power	155
CPU	156	Power	SSID	Power	156
	157	Power	SSID	Power	157
CPU	158	Power	SSID	Power	158
	159	Power	SSID	Power	159
CPU	160	Power	SSID	Power	160
	161	Power	SSID	Power	161
CPU	162	Power	SSID	Power	162
	163	Power	SSID	Power	163
CPU	164	Power	SSID	Power	164
	165	Power	SSID	Power	165
CPU	166	Power	SSID	Power	166
	167	Power	SSID	Power	167
CPU	168	Power	SSID	Power	168
	169	Power	SSID	Power	169
CPU	170	Power	SSID	Power	170
	171	Power	SSID	Power	171
CPU	172	Power	SSID	Power	172
	173	Power	SSID	Power	173
CPU	174	Power	SSID	Power	174
	175	Power	SSID	Power	175
CPU	176	Power	SSID	Power	176
	177	Power	SSID	Power	177
CPU	178	Power	SSID	Power	178
	179	Power	SSID	Power	179
CPU	180	Power	SSID	Power	180
	181	Power	SSID	Power	181
CPU	182	Power	SSID	Power	182
	183	Power	SSID	Power	183
CPU	184	Power	SSID	Power	184
	185	Power	SSID	Power	185
CPU	186	Power	SSID	Power	186
	187	Power	SSID	Power	187
CPU	188	Power	SSID	Power	188
	189	Power	SSID	Power	189
CPU	190	Power	SSID	Power	190
	191	Power	SSID	Power	191
CPU	192	Power	SSID	Power	192
	193	Power	SSID	Power	193
CPU	194	Power	SSID	Power	194
	195	Power	SSID	Power	195
CPU	196	Power	SSID	Power	196
	197	Power	SSID	Power	197
CPU	198	Power	SSID	Power	198
	199	Power	SSID	Power	199
CPU	200	Power	SSID	Power	200
	201	Power	SSID	Power	201
CPU	202	Power	SSID	Power	202
	203	Power	SSID	Power	203
CPU	204	Power	SSID	Power	204
	205	Power	SSID	Power	205
CPU	206	Power	SSID	Power	206
	207	Power	SSID	Power	207
CPU	208	Power	SSID	Power	208
	209	Power	SSID	Power	209
CPU	210	Power	SSID	Power	210
	211	Power	SSID	Power	211
CPU	212	Power	SSID	Power	212
	213	Power	SSID	Power	213
CPU	214	Power	SSID	Power	214
	215	Power	SSID	Power	215
CPU	216	Power	SSID	Power	216
	217	Power	SSID	Power	217
CPU	218	Power	SSID	Power	218
	219	Power	SSID		



PCH strap pin: [CFG3](#) [CFG4](#)

DISPLAY PORT PRESENCE STRAP	
CFG[4]	0 : ENABLED An external Display Port device is connected to the Embedded Display Port. 1 : DISABLED (Default) No Physical Display Port attached to Embedded DisplayPort*. No connect for disable.



Layout Note:
The total length of Data and Clock (from CPU to each VR) must be equal (40.2 inch).
Route the Data signal between VR Clock and VR Data signals.

SVID DATA

SVID CLOCK

SVID ALERT

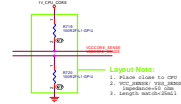


Figure 10-7. Routing Illustration for SVID Topology

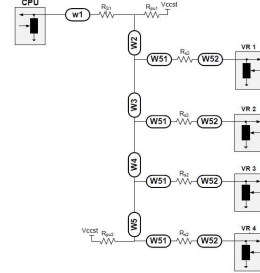
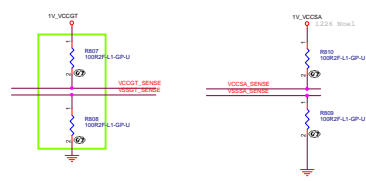
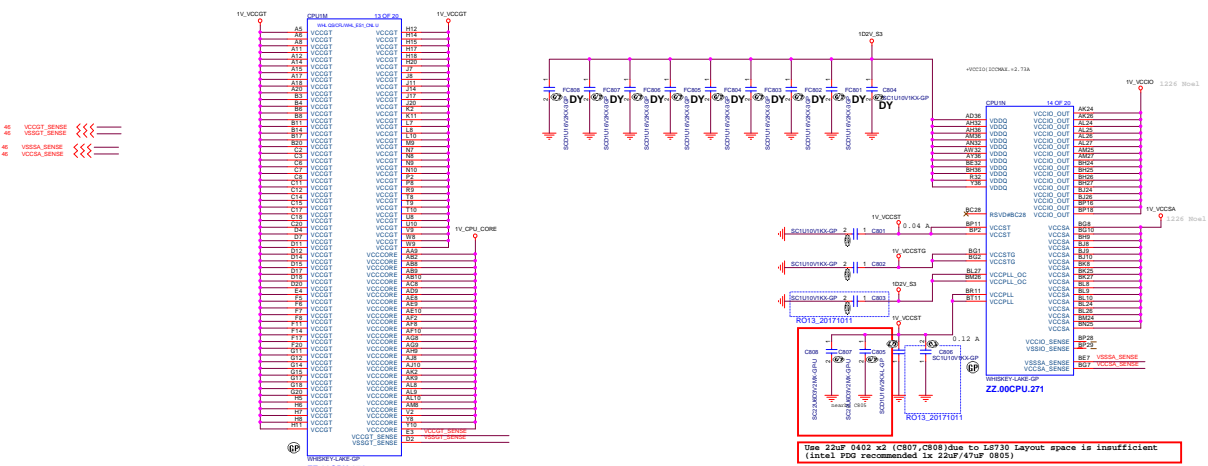


Table 10-10. SVID Bus Routing Guidelines

Signal	VR1 (inches)	VR2 (inches)	VR3 (inches)	VR4 (inches)	VR5 (inches)	VR6 (inches)	VR7 (inches)	VR8 (inches)	VR9 (inches)	VR10 (inches)
VIDOUT	0.5-3	1-15	0.5-4	3-17	<0.1	<0.1	Empty	45	0	50
VIDACK							56	Empty	220	0
VIDALERT										1.0



Vinafix.com

(Blanking)

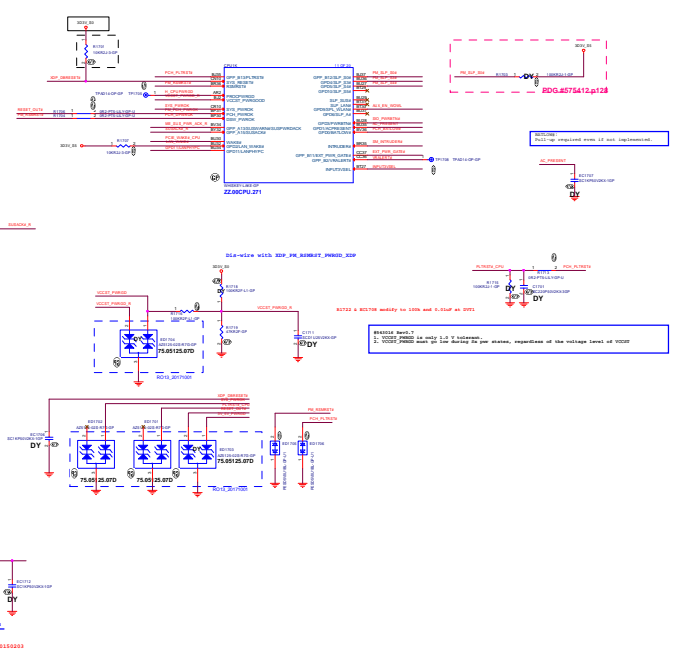
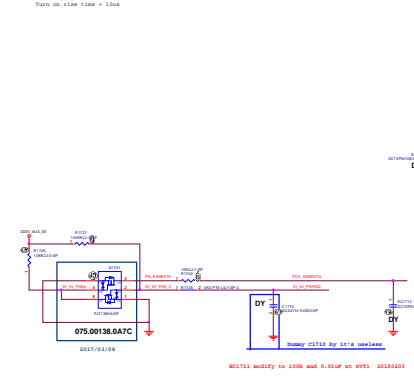
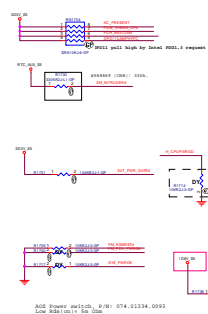
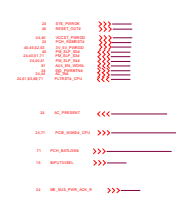
<Variant Name>		
緯創資通 Wistron Corporation 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title (Reserved)		
Size A3	Document Number S730	Rev -1
Date: Wednesday, July 26, 2018	Sheet 9 of	106

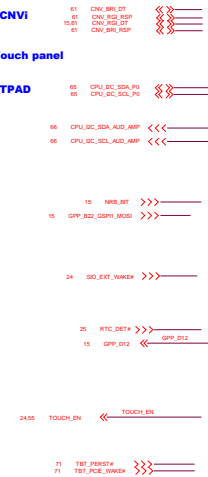
5	4	3	2	1
D				D
C				C
B				B
A				A
5	4	3	2	1

(Blanking)

<Variant Name>

緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title (Reserved)			
Size A4	Document Number S730		Rev -1
Date: Wednesday, July 25, 2018		Sheet 14 of	106





PDG #575412 p128



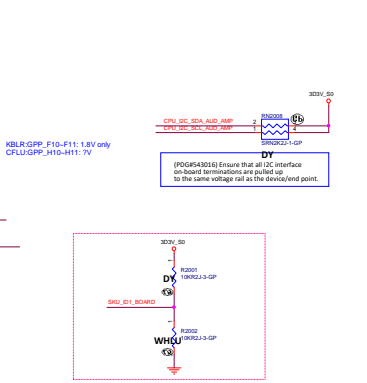
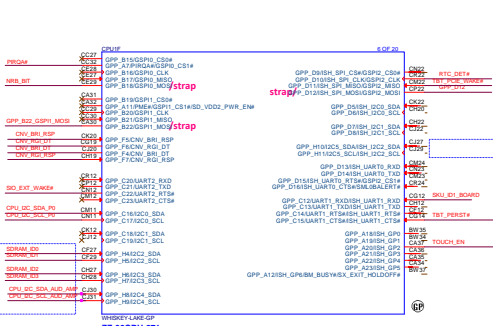
PCH strap pin: **NRB_BIT**

No Reboot	Sampled at rising edge of PCH_PWROK
GSPD_MOSI/ GPP_B18	0 = Disable 'No Reboot' mode. 1 = Enable 'No Reboot' mode (PCH will disable the TCO Timer system reboot feature). This function is useful when running TP30DP.

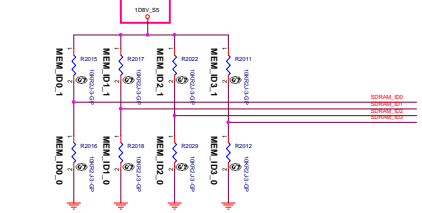
The signal has a weak internal pull-down.

TPAD Touch panel

KBLR GPP_F4.F5: 1.8V only
CFLR GPP_H10-H11: 1V



need to check power rail on GPP_H4~GPP_H8



GPP_H7	GPP_H6	GPP_H5	GPP_H4	SDRAM_ID3	SDRAM_ID2	SDRAM_ID1	SDRAM_ID0	Vendor PN	Wistron PN	Vendor	Density
0	0	0	0	0	0	0	0	KA8E324EB-EGCG		Samsung	4GB
0	0	0	1	0	0	0	1	H5CCNNNBSTALAR-NVD		HYUNIK	4GB
0	0	1	0	0	0	0	0	KA8E324EB-EGCG		MICRON	4GB
0	0	1	1	0	0	0	1	KA8E324EB-EGCG		Samsung	8GB
0	1	0	0	0	0	0	0	H5CCNNNBSTALAR-NVD		HYUNIK	8GB
0	1	0	1	0	0	0	1	MT52L512M3202DP-093 WT-B		MICRON	8GB
0	1	1	0	0	0	0	0			Samsung	12GB
0	1	1	1	0	0	0	1			HYUNIK	12GB
1	0	0	0	0	0	0	0			MICRON	16GB
1	0	0	1	0	0	0	1	KA8E324EB-EGCG		Samsung	16GB
1	0	1	0	0	0	0	1	H5CCNNNBSTALAR-NVD		HYUNIK	16GB
1	0	1	1	0	0	0	1	MT52L512M3202DP-093 WT-B		MICRON	16GB

0=1,1=H

Vinafix.com

[illegible]

Test point

AFTP2502 1 RTCVCC_CONN

AFTP2501 1

Vinafix.com

5	4	3	2	1
D				D
C				C
B				B
A				A
5	4	3	2	1

<Variant Name>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin-Tai Wu Rd., Hsueh-shan,
Taipei Hsien 221, Taiwan, R.O.C.

Title

AUDIO SPEAKER

Size

Document Number

Rev

A3

S730

-1

Date: Wednesday, July 25, 2018

Sheet 25 of 106

RESERVED

<Variant Name>		
緯創資通		Wistron Corporation
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taippei Hsien 221, Taiwan, R.O.C.
Title		
(RESERVED)		
Size	Document Number	Rev
A3	S730	-1
Date: Wednesday, July 26, 2018	Sheet 31 of	106

RESERVED

<Variant Name>

緯創資通		Wistron Corporation	
		21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
(RESERVED)			
Size	Document Number		Rev
Custom	S730		-1
Date:	Wednesday, July 25, 2018		Sheet 32 of 106

BLANK

<Variant Name>

緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title (RESERVED)			
Size A4	Document Number S730		Rev -1
Date: Wednesday, July 25, 2018		Sheet 33 of	106

BLANK

<Variant Name>			
緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
RESERVED			
Size	Document Number		Rev
A4	S730		-1
Date: Wednesday, July 25, 2018		Sheet 34 of	106

D

2

C

C

B

8

A

A

<Variant Name>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei 100, Taiwan, R.O.C.

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

(RESERVED)

Size	
------	--

Custom

	Document Number
--	-----------------

S730

Rev

-1

Date: Wednesday, July 25, 2018

Sheet 35 of 106

Vinafix.com

5	4	3	2	1
D				D
C				C
B				B
A				A
5	4	3	2	1

<Variant Name>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

USB RE-DRIVER

Size

Document Number

Rev

A3

S730

-1

Date: Wednesday, July 26, 2016

Sheet 37 of 106

5	4	3	2	1
D				D
C				C
B				B
A				A
5	4	3	2	1

<Variant Name>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin-Tai Wu Rd., Hsinchu,
Taipei Hsien 321, Taiwan, R.O.C.

Title

USB30 RE-DRIVER

Size

Document Number

Rev

A3

\$730

-1

Date: Wednesday, July 25, 2016

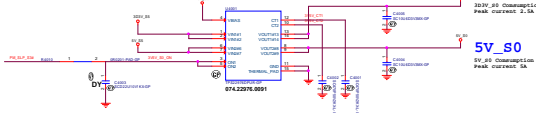
Sheet 36 of 106

BLANK

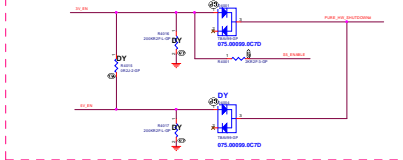
<Variant Name>

<div>緯創資通</div>		<div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>
Title (RESERVED)		
Size A4	Document Number S730	Rev -1
Date: Wednesday, July 25, 2018		Sheet 39 of 106

Run Power



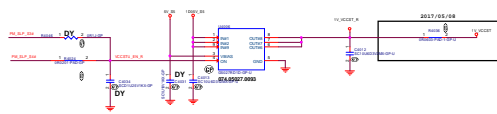
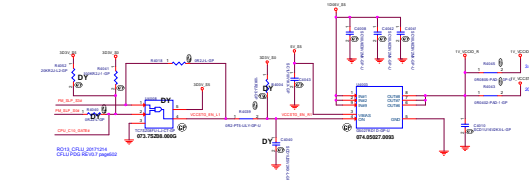
LV115 (-1M)



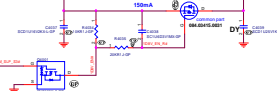
Power Good



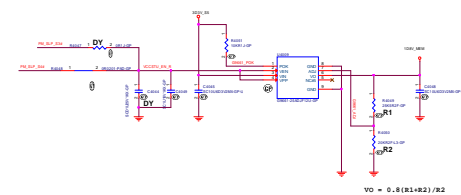
VCCIO and VCCSTG



1D8V_S0



1D8V_MEM

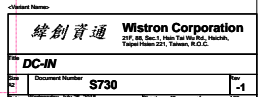


BLANK

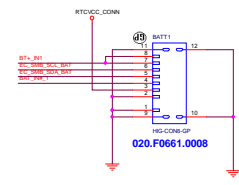
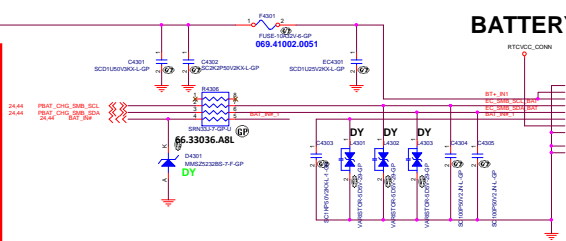
<Variant Name>

<div>緯創資通</div>		<div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>
Title RESERVED		
Size A4	Document Number S730	Rev -1
Date: Wednesday, July 25, 2018		Sheet 41 of 106

Need to apply WPN and symbol
084.08103.M001



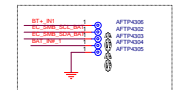
Battery PMOS page 44 has



Adaptor in to generate DCBATOUT

ADD_OFF and PMOS move to page44

08/06 add AD_OFF Circuit



PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

PH on EE Side

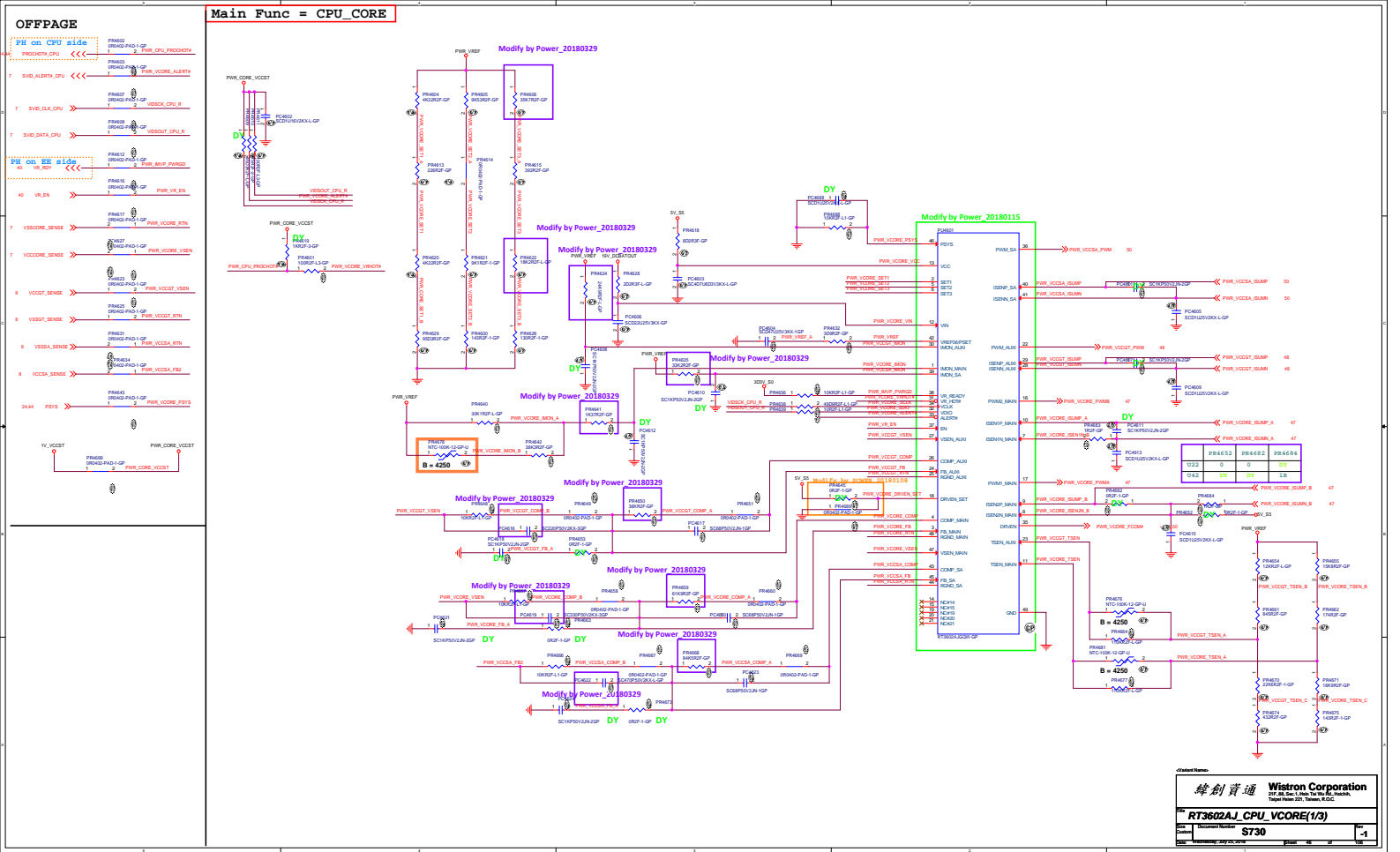
PH on EE Side

IDC: 6A
OCP: 8A

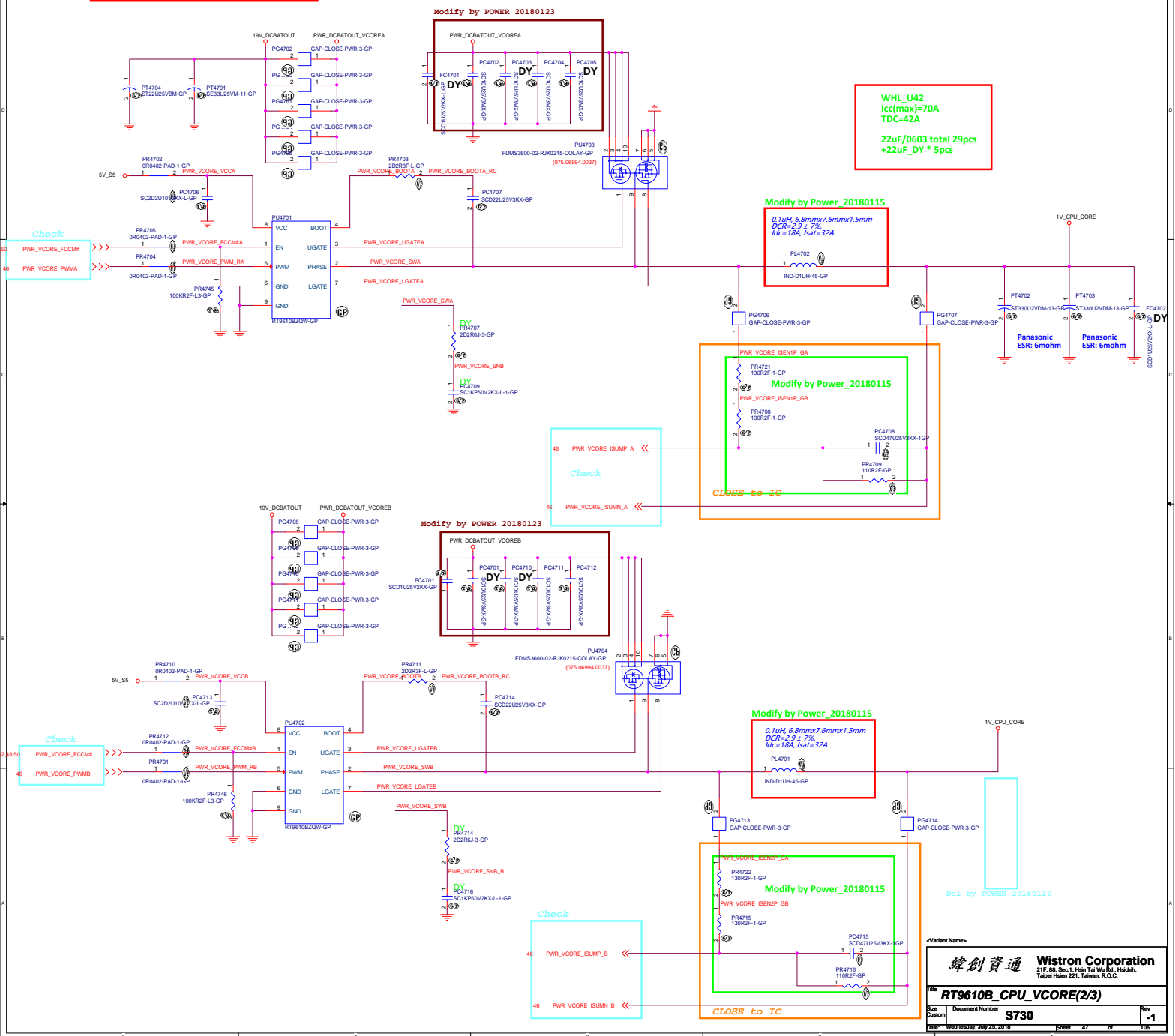
IDC: 8A

Vinafix.com

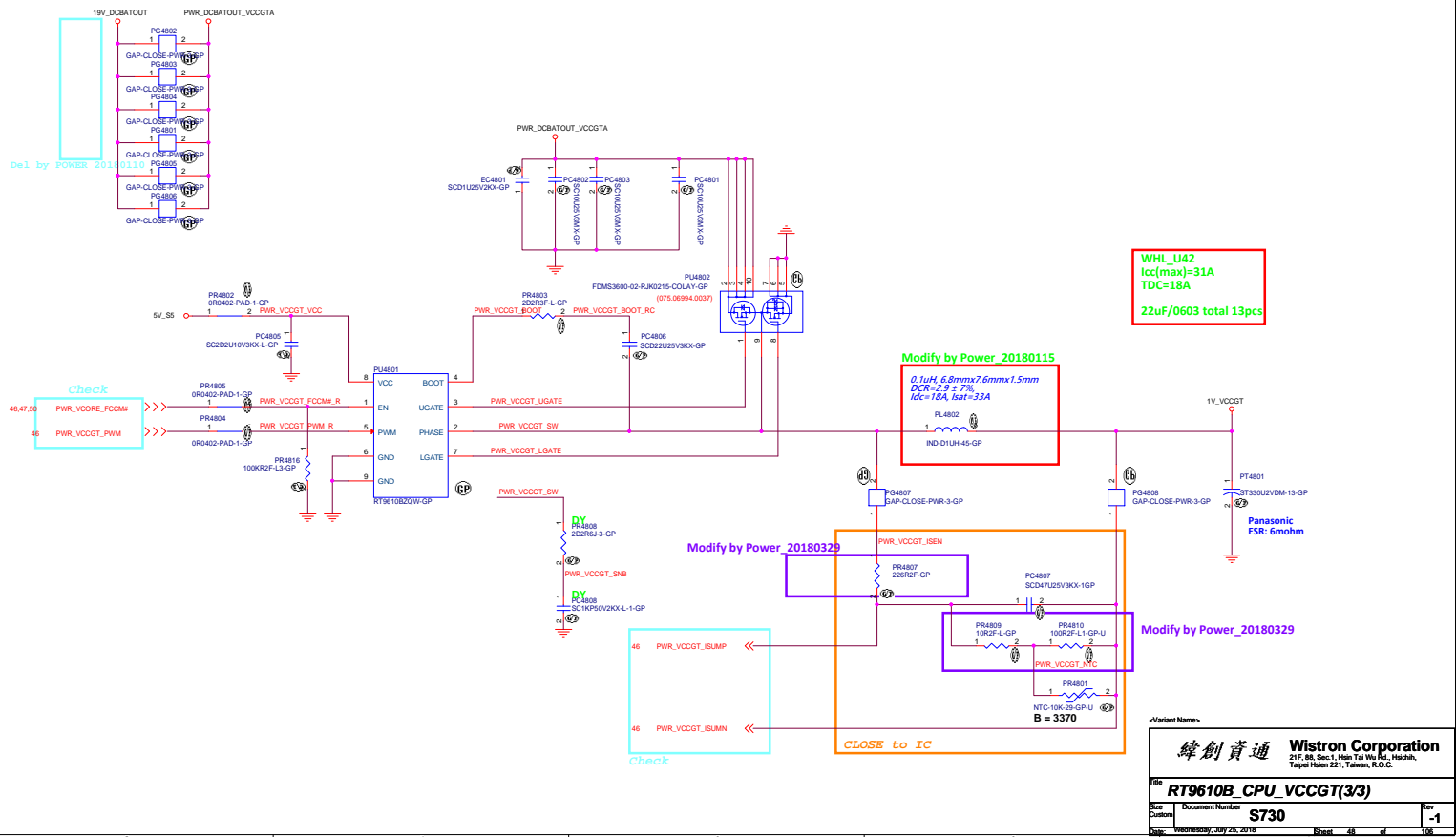
Wistron Corporation
SY8286 5V/3D3V
S730



Main Func = CPU_CORE



Main Func = CPU_CORE



5	4	3	2	1
Main Func = CPU_CORE				
D				D
C				C
B				B
A				A
BLANK				
5	4	3	2	1

<Variant Name>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

RESERVED

Size

Document Number

Rev

A4

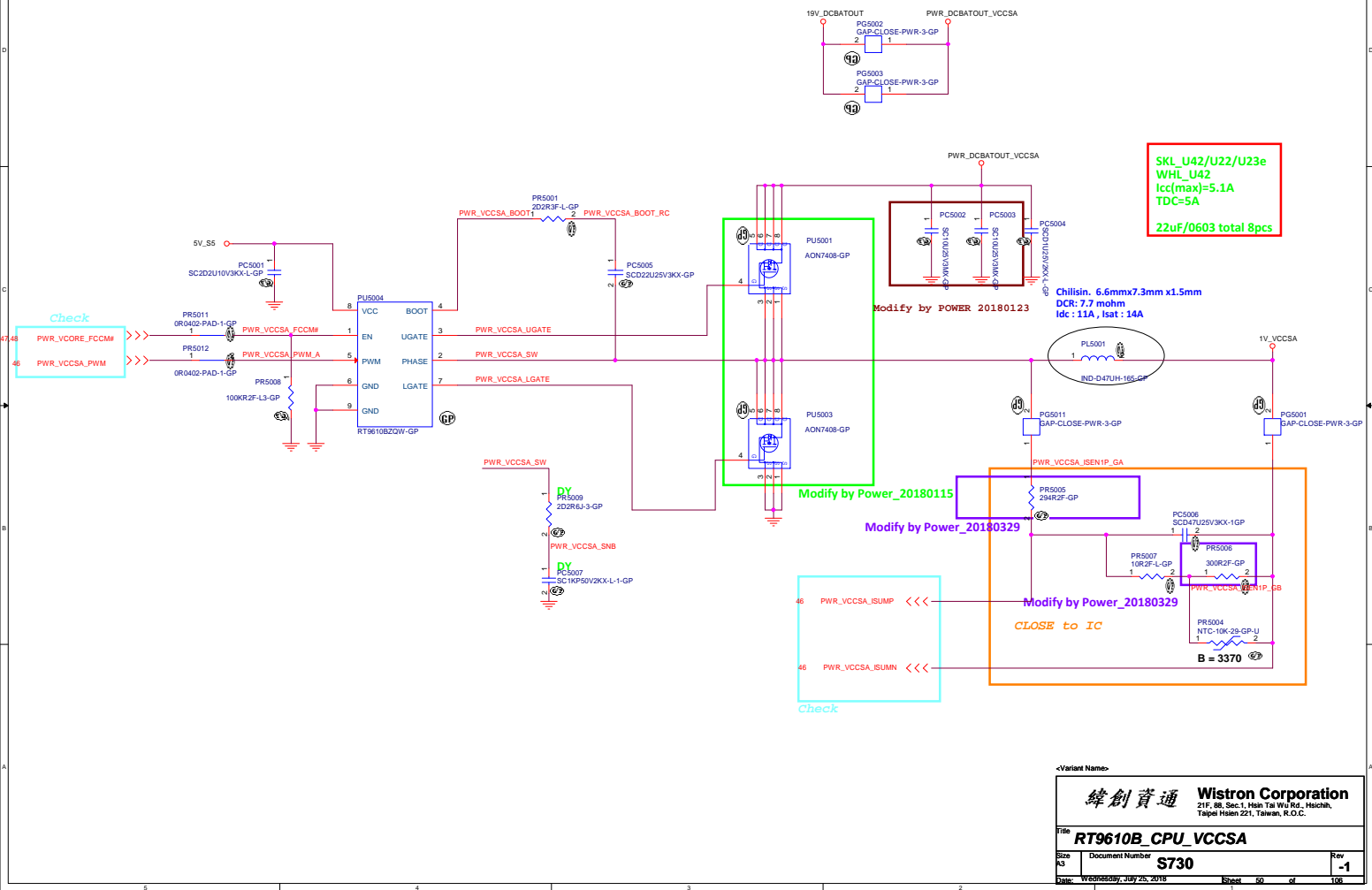
S730

-1

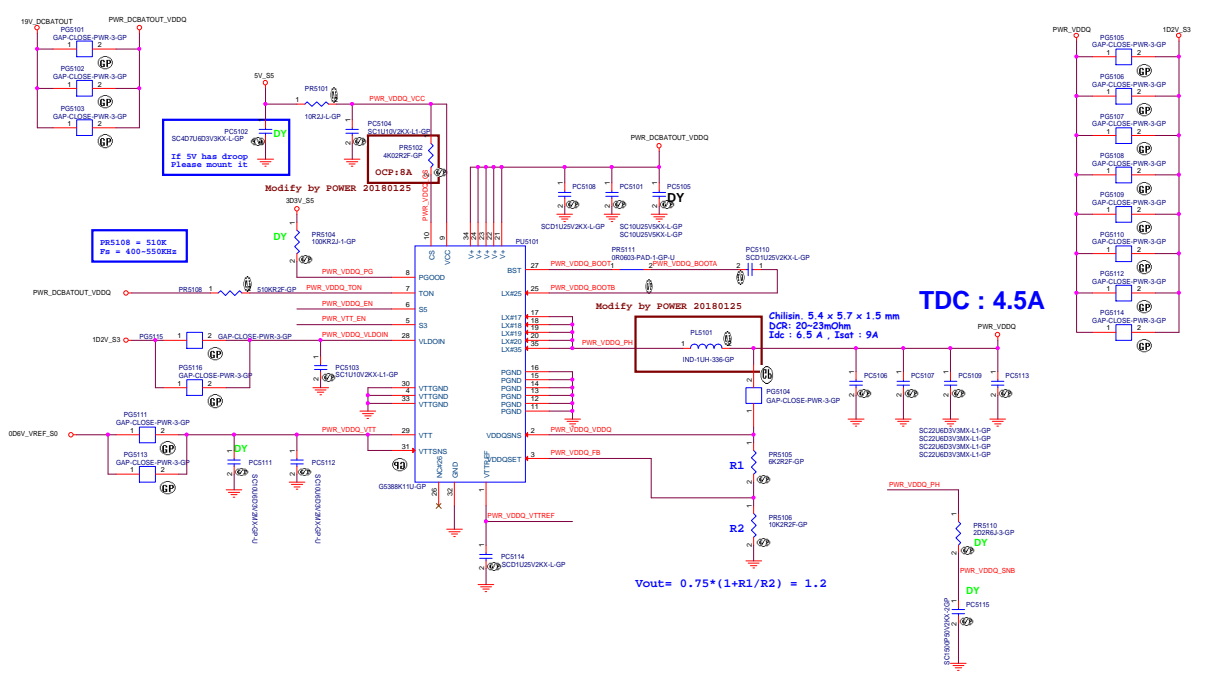
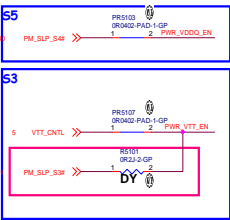
Date: Wednesday, July 25, 2018

Sheet 49 of 106

Main Func = CPU_CORE



OFFPAGE



Blanking

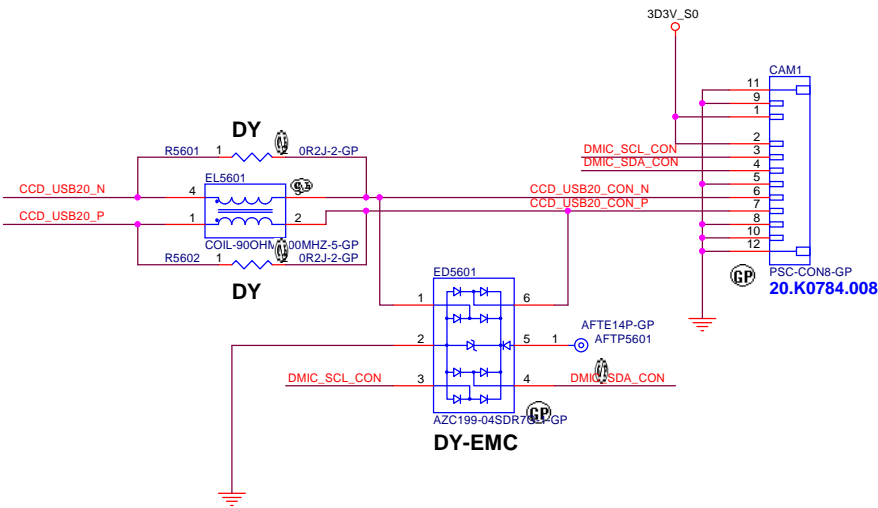
<Variant Name>

<div>緯創資通</div> <div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>	
Title RESERVED	
Size A4	Document Number S730
Date: Wednesday, July 25, 2018	Rev -1
Date: 2 Wednesday, July 25, 2018 Sheet 54 of 106	

CCD CONN

16 CCD_USB20_N << >>
16 CCD_USB20_P << >>

66 DMIC_SCL_CON >>
66 DMIC_SDA_CON >>



<Variant Name>

緯創資通		Wistron Corporation	
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title Display (CAM/DMIC)			
Size A4	Document Number S730	Rev -1	
Date: Wednesday, July 25, 2018		Sheet 56	of 106

BLANK

<Variant Name>

<div>緯創資通</div>		<div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>
Title (RESERVED)		
Size A4	Document Number S730	Rev -1
Date: Wednesday, July 25, 2018		Sheet 57 of 106

BLANK

<Variant Name>

<div>緯創資通</div> <div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>	
Title (RESERVED)	
Size A4	Document Number S730
Date: Wednesday, July 25, 2018	Rev -1
Date: Wednesday, July 25, 2018	
Sheet 58 of 106	

BLANK

<Variant Name>

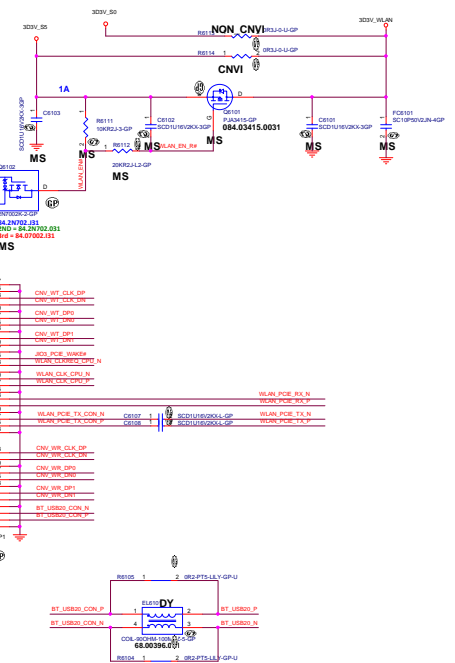
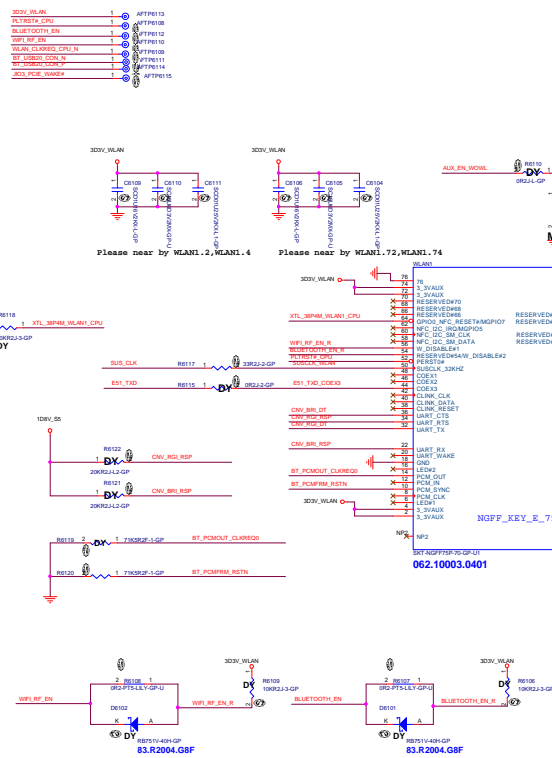
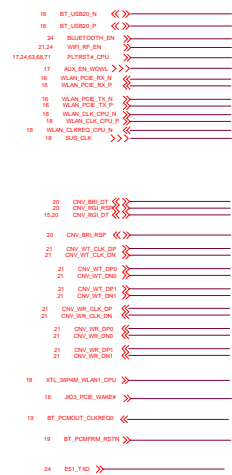
<div>緯創資通</div>		<div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>
Title Reserved		
Size A4	Document Number S730	Rev -1
Date: Wednesday, July 25, 2018		Sheet 59 of 106

BLANK

<Variant Name>

<div>緯創資通</div>		<div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>
Title RESERVED		
Size A4	Document Number S730	Rev -1
Date: Wednesday, July 25, 2018		Sheet 60 of 106

WLAN (CNVi)




BLANK

<Variant Name>

<div>緯創資通</div>		<div>Wistron Corporation</div>	
<div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>			
Title			
RESERVED			
Size	Document Number		Rev
A4	S730		-1
Date: Wednesday, July 25, 2018		Sheet	62 of 106
D		E	

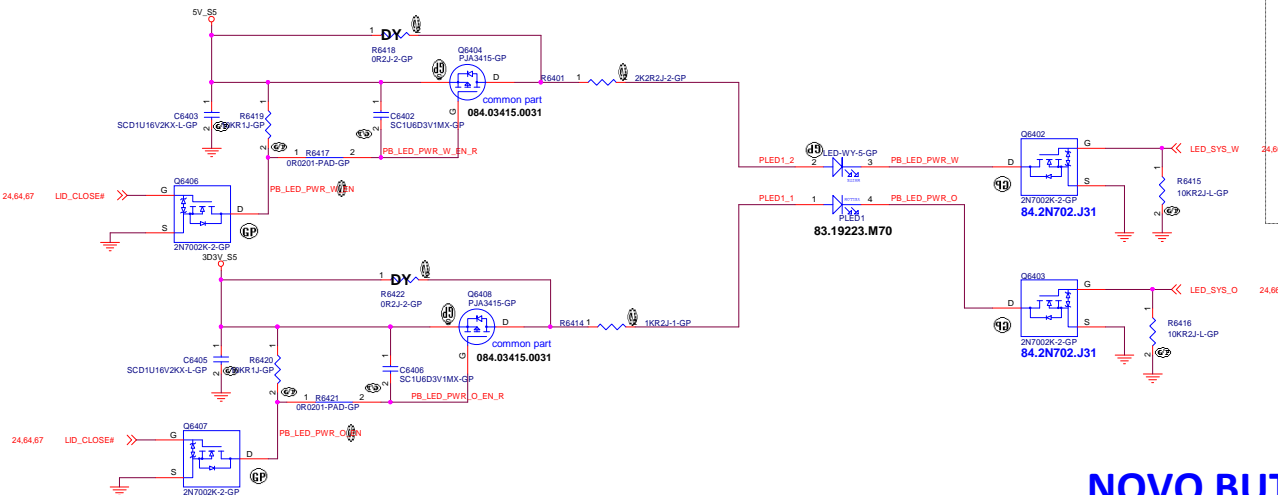
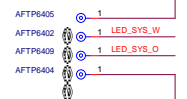
[illegible]

 緯創資通		Wistron Corporation 21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
File NGFF SSD SLOT			
Size Custom	Document Number S730		Rev -1
Date	Wednesday, July 26, 2018	Sheet	63 of 106

LED

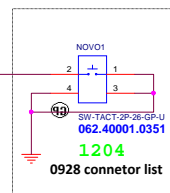
POWER BTN LED

Test point



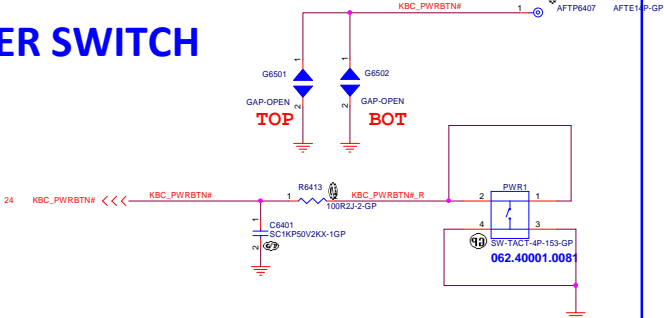
NOVO BUTTON

24 KBC_NOVO_BTN# >>> KBC_NOVO_BTN#



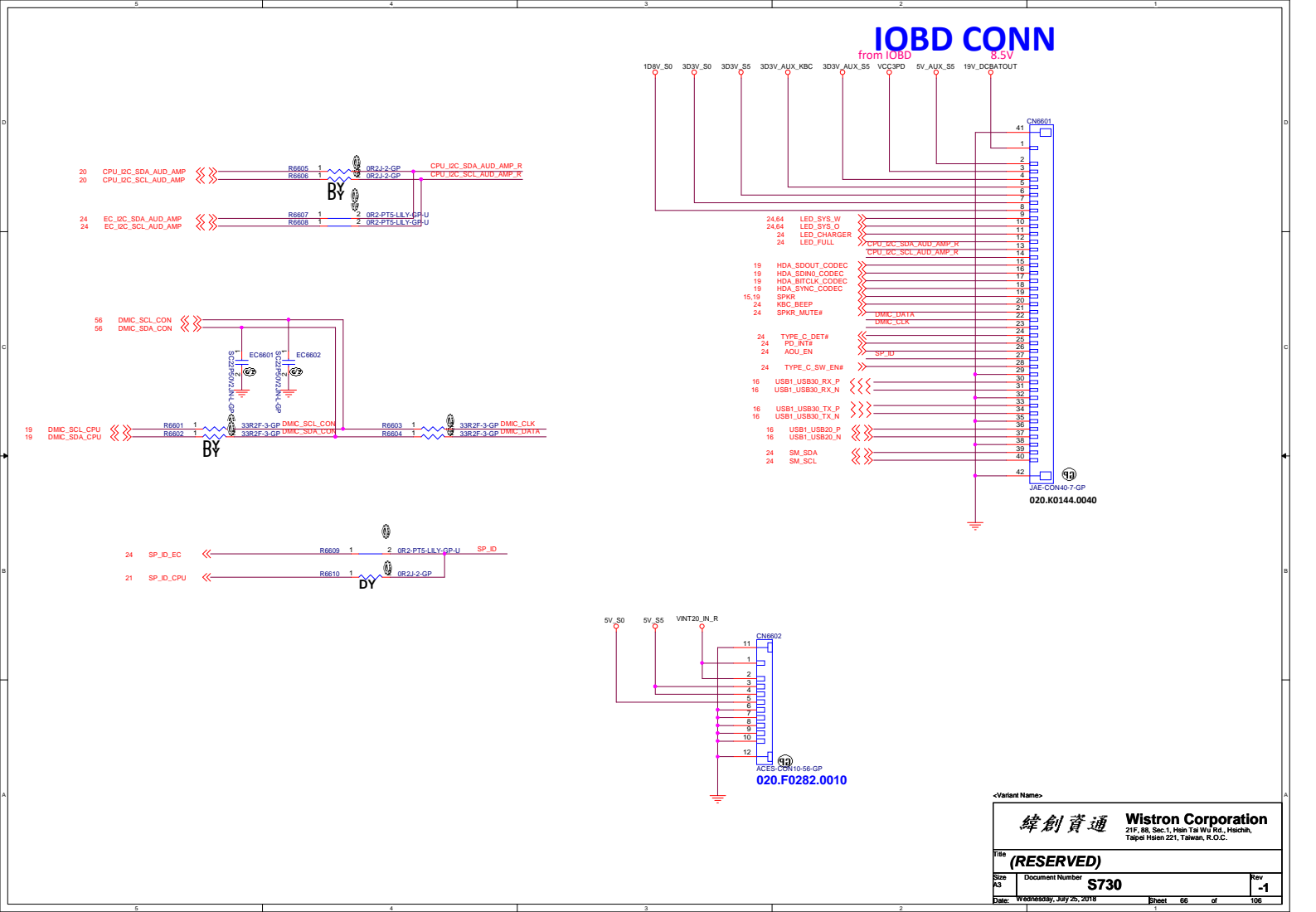
KBC_PWRBTN#

POWER SWITCH

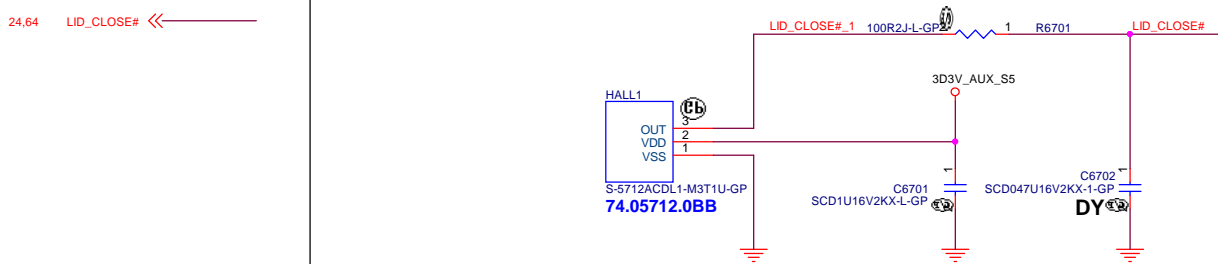


<Variant Name>

緯創資通 Wistron Corporation	
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
File	CHARGER LED/POWER BTN
Size	Document Number
	S730
Date: Wednesday, July 25, 2018	Rev -1
Sheet 64 of 106	



HALL SENSOR



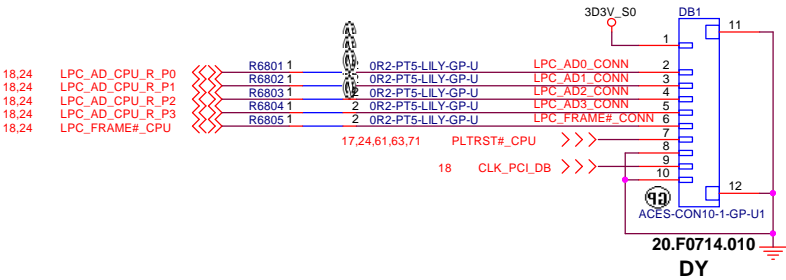
<Variant Name>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	HALL SENSOR (Hall sensor)
-------	----------------------------------

Size A4	Document Number S730	Rev -1
Date: Wednesday, July 25, 2018	Sheet 67 of 106	



<Variant Name>

緯創資通		Wistron Corporation	
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
Title DEBUG CONN			
Size A4	Document Number S730	Rev -1	
Date: Wednesday, July 25, 2018	Sheet 68 of 106		

5	4	3	2	1
D				D
C				C
B				B
A				A
5	4	3	2	1

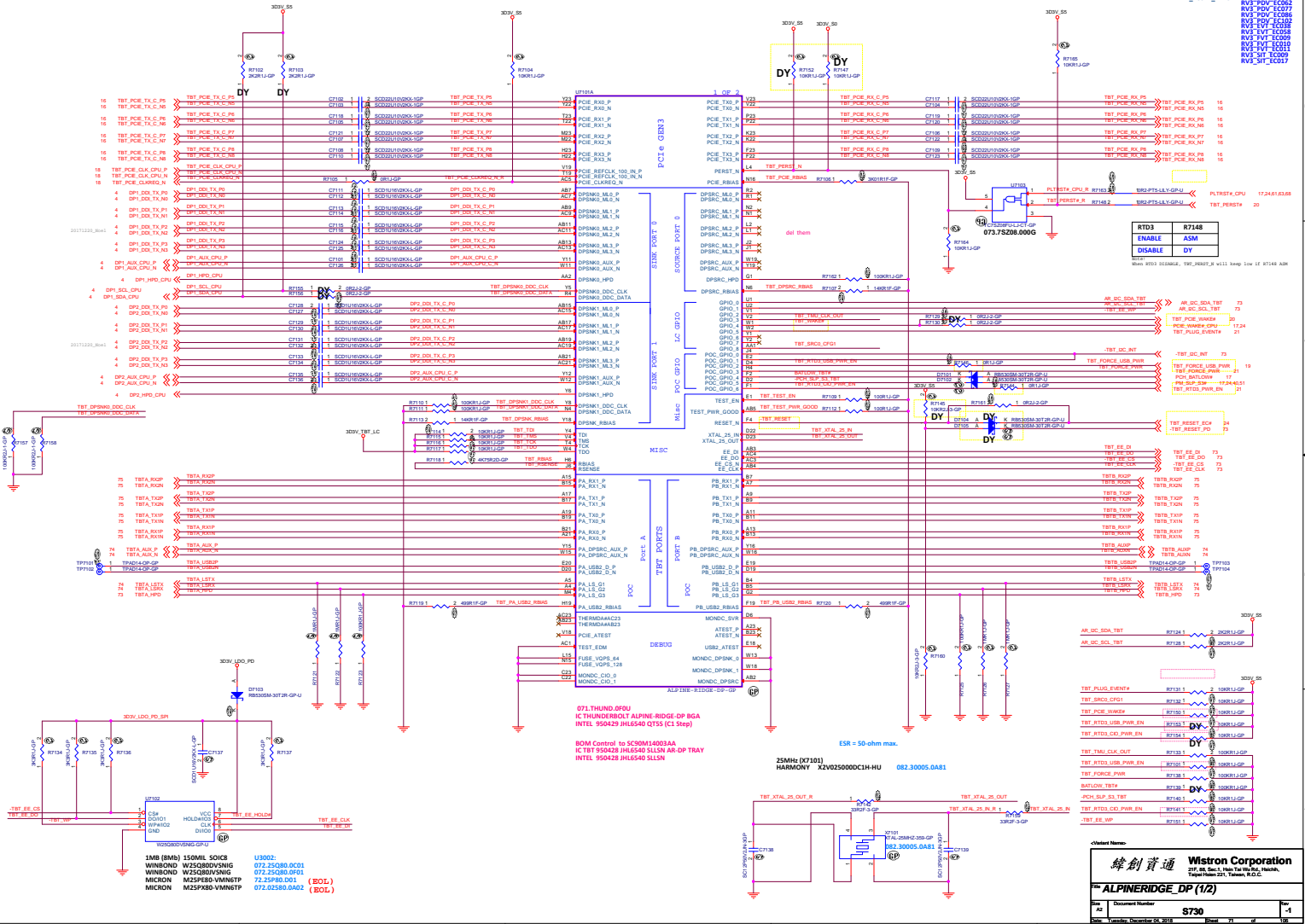
<Variant Name>

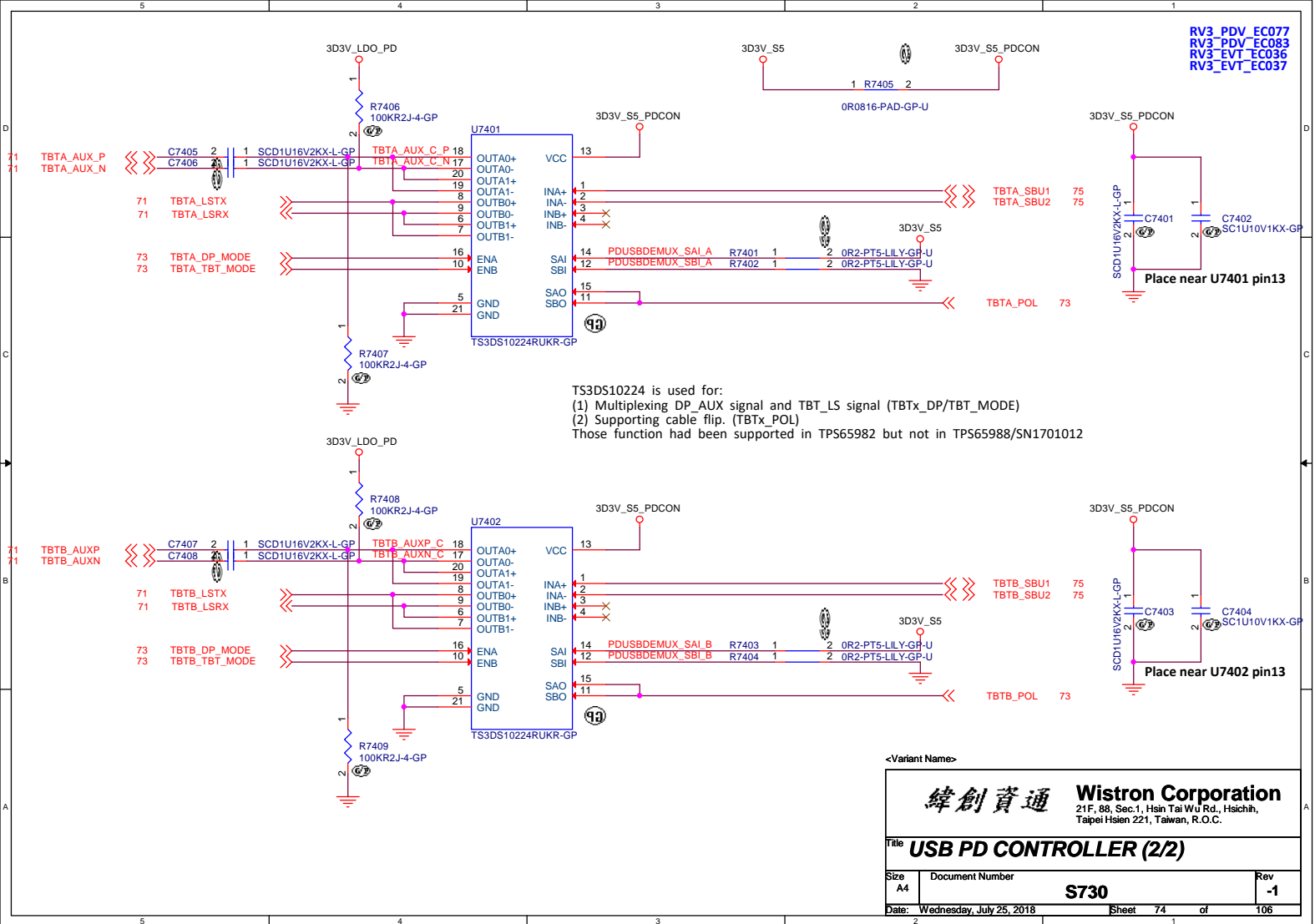
緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
Sensor (RSVD)			
Size	Document Number		Rev
A4	S730		-1
Date:	Wednesday, July 25, 2018	Sheet 69 of	106

BLANK

<Variant Name>

<div>緯創資通</div> <div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>	
Title (RESERVED)	
Size A4	Document Number S730
Date: Wednesday, July 25, 2018	Rev -1
Date: Wednesday, July 25, 2018	
Sheet 70 of 106	





BLANK

Vinafix.com

<Variant Name>

<div>緯創資通</div>		<div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>
Title RESERVED		
Size A4	Document Number S730	Rev -1
Date: Wednesday, July 25, 2018		Sheet 76 of 106

BLANK

<Variant Name>

<div>緯創資通</div> <div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>	
Title RESERVED	
Size A4	Document Number S730
Date: Wednesday, July 25, 2018	Rev -1
Date: 2 Wednesday, July 25, 2018 Sheet 77 of 106	

BLANK

<Variant Name>

<div>緯創資通</div>		<div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>
Title RESERVED		
Size A4	Document Number S730	Rev -1
Date: Wednesday, July 25, 2018		Sheet 78 of 106

BLANK

<Variant Name>

<div>緯創資通</div>		<div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>
Title RESERVED		
Size A4	Document Number S730	Rev -1
Date: Wednesday, July 25, 2018		Sheet 79 of 106

BLANK

<Variant Name>

<div>緯創資通</div> <div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>	
Title RESERVED	
Size A4	Document Number S730
Date: Wednesday, July 25, 2018	Rev -1
Date: Wednesday, July 25, 2018	
Sheet 80 of 106	

BLANK

<Variant Name>

<div>緯創資通</div>		<div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>
Title RESERVED		
Size A4	Document Number S730	Rev -1
Date: Wednesday, July 25, 2018		Sheet 81 of 106

BLANK

<Variant Name>

<div>緯創資通</div> <div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>	
Title RESERVED	
Size A4	Document Number S730
Date: Wednesday, July 25, 2018	Rev -1
Date: 2 Wednesday, July 25, 2018 Sheet 82 of 106	

BLANK

<Variant Name>

<div>緯創資通</div> <div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>	
Title RESERVED	
Size A4	Document Number S730
Date: Wednesday, July 25, 2018	Rev -1
Date: 2 Wednesday, July 25, 2018 Sheet 83 of 106	

BLANK

<Variant Name>

<div>緯創資通</div>		<div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>
Title RESERVED		
Size A4	Document Number S730	Rev -1
Date: Wednesday, July 25, 2018		Sheet 84 of 106

BLANK

<Variant Name>			
緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
GPU CORE			
Size	Document Number		Rev
A4	S730		-1
Date: Wednesday, July 25, 2018		Sheet 85 of	106

BLANK

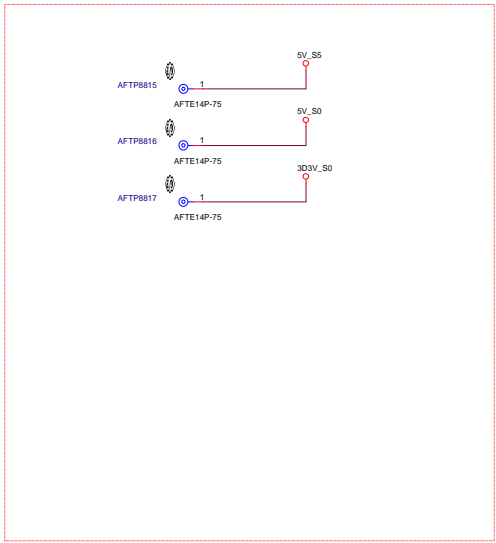
<Variant Name>

緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
GPU Discrete Power			
Size	Document Number		Rev
A4	S730		-1
Date: Wednesday, July 25, 2018		Sheet 86 of	106

BLANK

<Variant Name>

<div>緯創資通</div>		<div>Wistron Corporation</div>	
<div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>			
Title			
Reserved			
Size	Document Number		Rev
A4	S730		-1
Date: Wednesday, July 25, 2018		Sheet 87 of 106	



20170811 EC027
20170811 EC040

<Variant Name>		
<div><div>緯創資通</div><div>Wistron Corporation</div><div>ZIF, 88, Sec.1, Hsin Tai Wu Rd., Hoshih, Taipei Hsien 221, Taiwan, R.O.C.</div></div>		
File		
Size	Docu. Number	Rev
Custom	TEST POINT S730	-1
Date	Wednesday, July 25, 2018	Sheet 88 of 106

5

4

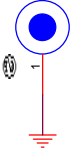
3

2

1

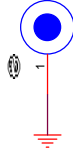
434.0FD02.0001

HS1
STF236R128H22-4-GP



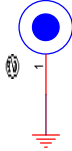
434.0FD02.0001

HS2
STF236R128H22-4-GP



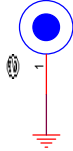
434.0FD02.0001

HS3
STF236R128H22-4-GP



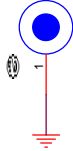
434.0FD02.0001

HS4
STF236R128H22-4-GP



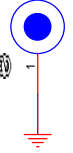
434.0FD02.0001

H8
STF236R128H22-4-GP



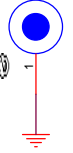
434.0FD01.0001

HS7
STFT237B158R128H48-GP



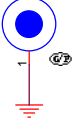
434.0FD01.0001

HS8
STFT237B158R128H48-GP



ZZ.00PAD.GG1

H2
HT9X9B9X9R31-S-GP



Vinafix.com

<Variant Name>

<div>緯創資通</div> <div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>		
Title		
UNUSED PARTS/EMI Capacitors		
Size A4	Document Number S730	Rev -1
Date: Wednesday, July 25, 2018	Sheet 89	of 106

BLANK

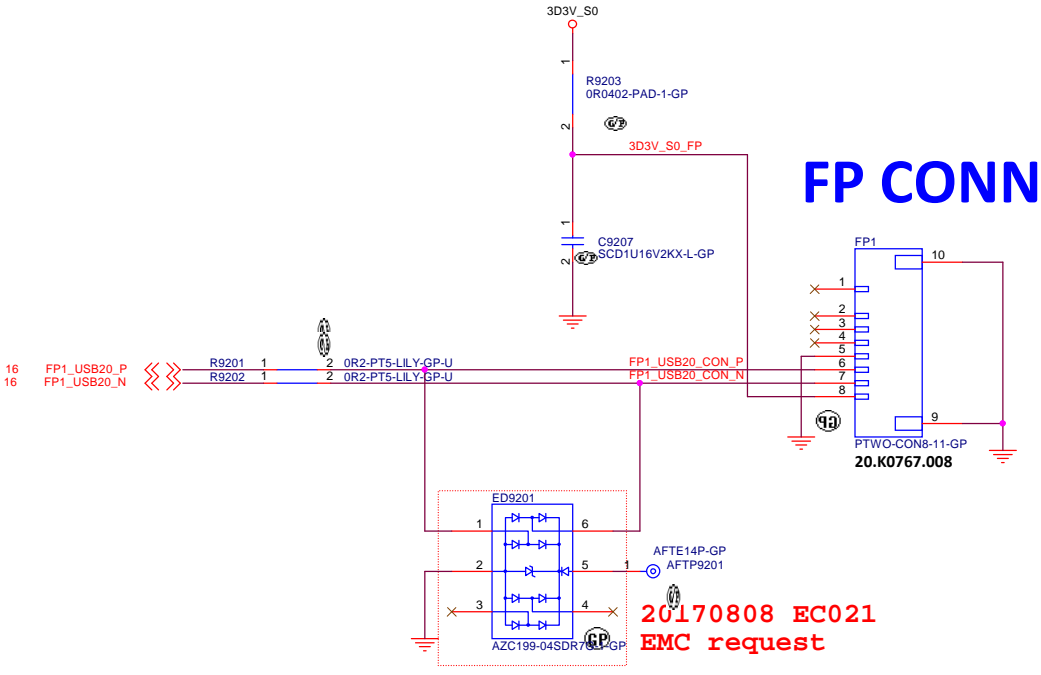
<Variant Name>

<div>緯創資通</div>		<div>Wistron Corporation</div>	
<div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>			
<div>Title</div>			
<div>(RESERVED)</div>			
<div>Size</div>	<div>Document Number</div>		<div>Rev</div>
<div>A4</div>	<div>S730 teknisi indonesia</div>		<div>-1</div>
<div>Date: Wednesday, July 25, 2018</div>		<div>Sheet 90 of 106</div>	

BLANK

<Variant Name>

<div>緯創資通</div> <div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>	
Title (RESERVED)	
Size A4	Document Number <div>S730</div>
Date: Wednesday, July 25, 2018	Rev -1
Date: Wednesday, July 25, 2018Sheet 91 of 106	



<Variant Name>

緯創資通 Wistron Corporation		
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title		
Finger Print		
Size A4	Document Number S730	Rev -1
Date: Wednesday, July 25, 2018	Sheet 92 of 106	

8081

緯創資通		Wistron Corporation	
21F, No. 224-1, Hsin-Tai Rd., Sec. 2, Taipei 105, Taiwan, R.O.C.		21F, No. 224-1, Hsin-Tai Rd., Sec. 2, Taipei 105, Taiwan, R.O.C.	
TYPEC PD Controller			
S730		-1	
Rev. 1.00		Rev. 1.00	

5

4

3

2

1

D

D

C

C

B

B

A

A

BLANK

Vinafix.com

<Variant Name>

<div><div>緯創資通</div><div>Wistron Corporation</div><div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div></div>		
Title		
Reserved		
Size	Document Number	Rev
A4	S730	-1
Date: Wednesday, July 25, 2018		Sheet 95 of 106
2		1

BLANK

<Variant Name>

<div>緯創資通</div>		<div>Wistron Corporation</div>	
<div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>			
Title			
Reserved			
Size	Document Number		Rev
A4	S730		-1
Date: Wednesday, July 25, 2018		Sheet	96 of 106
2		1	

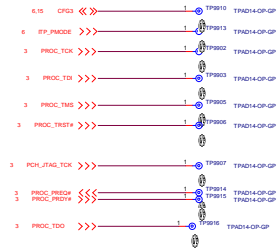
BLANK

<Variant Name>

緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
Reserved		Vinafix.com	
Size	Document Number	Rev	
A4	S730	-1	
Date:	Wednesday, July 25, 2018	Sheet 97 of	106
2		1	

BLANK

<Variant Name>			
緯創資通		Wistron Corporation	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
Reserved			
Size	Document Number		Rev
A4	S730		-1
Date: Wednesday, July 25, 2018		Sheet 98 of	106



5	4	3	2	1
D				D
C				C
B				B
A				A
5	4	3	2	1
				<Variant Name>
				<div><div>緯創資通Wistron Corporation</div><div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div></div>
				Title
				Table of Content
				Size A4
				Document Number
				S730
				Rev
				-1
				Date: Wednesday, July 25, 2018
				Sheet 100 of 106

5	4	3	2	1
D				D
C				C
B				B
A				A
5	4	3	2	1

<Variant Name>

緯創資通

Wistron Corporation
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

Change History

Size
A3

Document Number
S730

Rev
-1

Date: Wednesday, July 26, 2018

Sheet 101 of 106

D

D

C

C

B

B

A

A

<Variant Name>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

Power Sequence

Size
A

Document Number

S730

Rev

-1

Date: Wednesday, July 25, 2018

Sheet 102 of 106

D

D

C

C

B

B

A

A

<Variant Name>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	Author	Year	Journal	Volume	Issue	Page
1. The Effect of Temperature on the Rate of Reaction	John Doe	2018	Journal of Chemical Education	95	3	456-462
2. Kinetics of the Reaction Between Hydrogen Peroxide and Potassium Iodide	Jane Smith	2017	Journal of Chemical Education	94	2	321-328
3. The Effect of Concentration on the Rate of Reaction	Michael Brown	2016	Journal of Chemical Education	93	1	123-130
4. The Effect of Surface Area on the Rate of Reaction	Sarah White	2015	Journal of Chemical Education	92	4	567-574
5. The Effect of Catalyst on the Rate of Reaction	David Green	2014	Journal of Chemical Education	91	5	678-685

Power Block Diagram

Size
A

Document Number

S730

Rev

-1

Date: Wednesday, July 25, 2018

Sheet 103 of 106

D

D

C

C

B

B

A

A

<Variant Name>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title

SMBUS/I2C BLOCK DIAGRAM

Size
A

Document Number

S730

Rev

-1

Date: Wednesday, July 25, 2018

Sheet 104 of 106

D

2

C

C

B

1

A

A

<Variant Name>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	Author	Year	Journal	Volume	Issue	Page
1. The Effect of Temperature on the Rate of Reaction	John Doe	2018	Journal of Chemical Education	95	3	456-462
2. Kinetics of the Reaction Between Hydrogen Peroxide and Potassium Iodide	Jane Smith	2017	Journal of Chemical Education	94	2	234-240
3. The Effect of Concentration on the Rate of Reaction	Michael Brown	2016	Journal of Chemical Education	93	1	123-129
4. The Effect of Surface Area on the Rate of Reaction	Sarah White	2015	Journal of Chemical Education	92	4	567-573
5. The Effect of Catalyst on the Rate of Reaction	David Green	2014	Journal of Chemical Education	91	5	678-684

THERMAL/AUDIO BLOCK DIAGRAM

Size
A

A

Document Number

S730

Rev

-1

Date: Wednesday, July 25, 2018

Sheet 105 of 106

D

2

C

C

B

1

A

A

<Variant Name>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,
Taipei Hsien 221, Taiwan, R.O.C.

Title	Author	Year	Journal	Volume	Issue	Page
1. The Effect of Temperature on the Rate of Reaction	John Doe	2018	Journal of Chemical Education	95	3	456-462
2. Kinetics of the Reaction Between Hydrogen Peroxide and Potassium Iodide	Jane Smith	2017	Journal of Chemical Education	94	2	234-240
3. The Effect of Concentration on the Rate of Reaction	Michael Brown	2016	Journal of Chemical Education	93	1	123-129
4. The Effect of Surface Area on the Rate of Reaction	Sarah White	2015	Journal of Chemical Education	92	4	567-573
5. The Effect of Catalyst on the Rate of Reaction	David Green	2014	Journal of Chemical Education	91	5	678-684

CLK Block

Size
A

Document Number

S730

Rev

-1

Date: Wednesday, July 25, 2018

Sheet 106 of 106

